

CLAIMS

What is claimed is:

- 5 1. A termination stub system comprising:
a first resistor for dampening reflections of a signal, said first resistor in series
with an input signal path;
a division point for dividing said signal into a plurality of output
communication paths, said division point coupled to said first resistor; and
10 a second resistor for balancing resistance of a termination stub system with a
characteristic impedance of said signal input path, said second resistor in parallel with
said input signal path and said plurality of output communication paths and said
second resistor coupled to said first resistor.
- 15 2. A termination stub system of Claim 1 wherein said first resistor and said second
resistor form a voltage divider for reducing a voltage level of said signal at said
division point.
3. A termination stub system of Claim 1 wherein said first resistor is sized to
20 reduce overshoot of said signal at receivers coupled to said plurality of output
communication paths.
4. A termination stub system of Claim 1 wherein said second resistor is coupled to
a termination voltage.

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5. A termination stub system of Claim 5 wherein said termination voltage is a steady state voltage.

6. A termination stub system of Claim 1 wherein said termination said input
5 signal path and said plurality of output communication paths are trace lines in printed circuit board.

7. A termination stub system of Claim 1 wherein said first resistor is coupled immediately to said second resistor.

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8. An integrated circuit comprising:
a plurality of receivers for receiving a signal;
a driver for driving said signal to said plurality of receivers; and
a termination stub system for directing said signal to said receivers while
15 minimizing reflection of said signal towards said driver.

9. An integrated circuit of claim 8 further comprising a plurality of branch signal communication paths coupled to said plurality of receivers.

20 10. An integrated circuit of claim 9 wherein said plurality of branch signal communication paths are the same length.

11. An integrated circuit of claim 10 wherein said plurality of branch signal communication paths are configured to deliver said signal .

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12. An integrated circuit of claim 11 wherein said signal is a control signal.

13. An integrated circuit of claim 12 wherein said receivers are memory components included in a memory system and said control signal controls said memory components.

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14. An integrated circuit of claim 13 wherein said control signals are chip select signals.

15. A termination stub method comprising:

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forwarding a signal to a single distribution point;
distributing said signal to a plurality of destinations; and
reducing reflectance of said signal.

16. A termination stub method of claim 15 further comprising managing overshoot conditions.

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17. A termination stub method of claim 15 wherein said distributing further comprises:

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receiving a signal along a single path; and
splitting said signal into a plurality of signal wave fronts;

18. A termination stub method of claim 15 wherein said reducing said reflectance comprises:

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supplying a termination voltage; and
matching a characteristic transmission impedance.

19. A termination stub method of claim 15 wherein wave fronts of said signal are received concurrently

20. A termination stub method of claim 15 further comprising directing said signal
5 along a plurality of communication paths that are the same length